

What is claimed is:

1. A spectrum spread receiver, in which, when conducting RAKE synthesis of inverse spread data generated based on received data, RAKE synthesis is executed by performing a predetermined timing adjustment after the inverse spread data having a plurality of symbols are first stored in a memory circuit.
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2. A spectrum spread receiver, in which received data from multiple paths are correlatively processed to be in synchronism with a timing corresponding to each of said paths, and a correlative output signal of each of said paths is synthesized by RAKE synthesis, comprising:
 - 5 a plurality of finger processing circuits for generating inverse spread data in accordance with said timings;
 - a memory circuit for storing said inverse spread data of a unit length;
 - a timing adjustment circuit for outputting a timing signal when a predetermined amount of inverse spread data is stored in said memory circuit; and
 - 10 a RAKE synthesizer for performing the RAKE synthesis by reading a unit length of inverse spread data from said memory circuit based on the timing signal output from said timing adjustment circuit.
3. A spectrum spread receiver according to claim 2, wherein said timing adjustment circuit outputs a timing signal for performing the RAKE synthesis when a predetermined amount of inverse spread data is stored in said memory circuit.
4. A spectrum spread receiver according to claim 2, wherein

each of said finger processing circuits generates and outputs inverse spread data and an inverse spread data output flag whenever a predetermined length of inverse spread data is completed; and

- 5 said timing adjustment circuit writes said inverse spread data in said memory circuit at a timing when said inverse spread data output flag is input, by referring to said inverse spread data output flag generated and output from each of said finger processing circuits, when each of said finger processing circuits has a different processing timing.

- 5 5. A spectrum spread receiver according to claim 4, wherein an address, generated at the time when said inverse spread data is written, is determined by a finger number, a symbol number processed by said finger processing circuit, and a variable value which is alternately set to "0" or "1" when a predetermined amount of inverse spread data is stored in said memory circuit.